

10/550559

JC20 Rec'd PCT/PTO 23 SEP 2005

XA-10437

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

NISHIZAWA, Hirotaka et al.

Intl. Appln. No.: PCT/JP2004/013930

Intl. Filing Date: 24 September 2004

For: SEMICONDUCTOR DEVICE

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Mail Stop PCT, DO/EO/US

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

The document on the attached List was cited in the specification, on pages 1-2, and its relevance is indicated therein.

Respectfully submitted,

MWS:sjk

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By: 

Mitchell W. Shapiro
Reg. No. 31,568

September 23, 2005

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| FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT LIST OF DOCUMENTS CITED BY APPLICANT | | | | Atty. Docket No. XA-10437 | | Appln. No. 10/550559 | |
| | | | | Applicant NISHIZAWA, Hirotaka et al. | | | |
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| U.S. PATENT DOCUMENTS | | | | | | | |
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| FOREIGN PATENT DOCUMENTS | | | | | | | |
| Examiner Initial | | Document Number | Date | Country | Class | Sub-class | Translation |
| | AE | 2000-99215 | 04/07/00 | Japan | | | Yes |
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